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APPLICANTS Junji Ichimiya, Osaka, JAPAN; ** CONTINUING DATA ***** <i>None TT</i> ** FOREIGN APPLICATIONS ***** <i>Verified</i> JAPAN 2002-320912 11/05/2002 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/03/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Allowance <i>TT</i> Acknowledged <i>TT</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 18	TOTAL CLAIMS <i>12</i> 15	INDEPENDENT CLAIMS 2
ADDRESS Ivan S. Kavrukov, Esq. Cooper & Dunham LLP 1185 Avenue of the Americas New York, NY 10036					
TITLE Layout Design Method of Semiconductor Integrated Circuit, and Semiconductor Integrated Circuit, with High Integration Level of Multiple Level Metalization					
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		